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UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,838,889 B2

DATED

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INVENTOR(S) : Su et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 7,

Lines 32 and 33, "boost circuit" should read -- boost module --Line 35, "boost circuit" should read -- boost module --

Signed and Sealed this

Tenth Day of May, 2005

JON W. DUDAS Director of the United States Patent and Trademark Office